



## Fujitsu FLASHWAVE® 7500 BITS Streaker/Test Board STB-FUJ7500BITS Practice



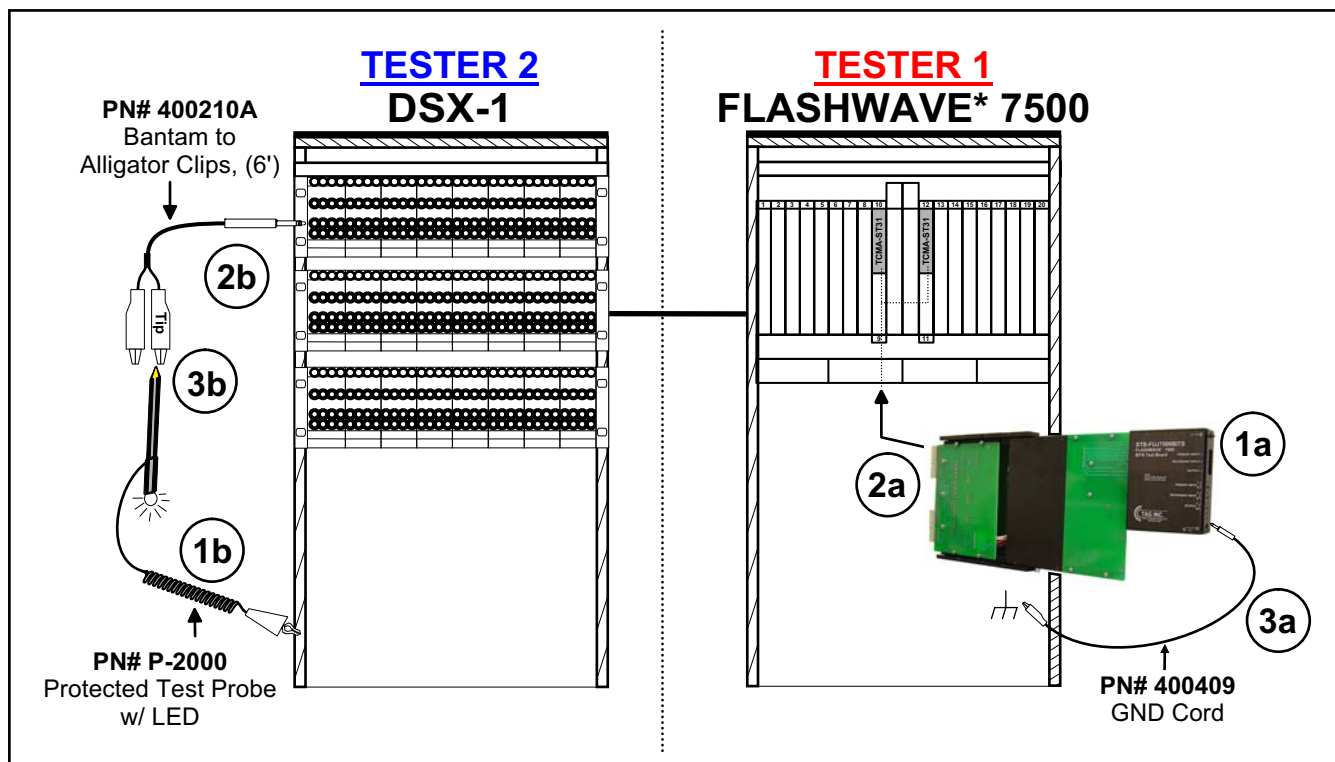
The STB-FUJ7500BITS provides a quick method to verify continuity as well as, transmit a test signal from the FLASHWAVE 7500 BITS card slots to the DSX-1. At the DSX-1, Tester 2 will use a continuity test probe to verify that the BITS card slot is properly wired. Tester 2 is also capable of looping a test signal back to the STB-FUJ7500BITS and Tester 1 can determine if the signal is acceptable for system turnup. The STB-FUJ7500BITS is used on non-powered systems.





## Fujitsu FLASHWAVE® 7500 BITS Streaker/Test Board STB-FUJ7500BITS Practice

### ► Step by Step Procedure



### TESTER 1 (Fujitsu FLASHWAVE 7500 BITS Continuity Test)

- 1a. Press LED test button. Verify that all LED's illuminate. If LED's do not illuminate, replace with a new battery.
- 2a. Select BITS card slot TCMA-ST31 to begin testing.  
**(CAUTION: Do not force. Verify proper alignment before inserting.)**
- 3a. If chassis ground is not already connected through the backplane, insert Pin plug test cord (#400409) into the STB-FUJ7500BITS Card and connect the Alligator Clip to Frame Ground.
- 4a. Establish communication with Tester 2 at the DSX-1. You are ready to begin testing at the Fujitsu FLASHWAVE 7500 (observe LED's illuminating).

### TESTER 2 (DSX-1)

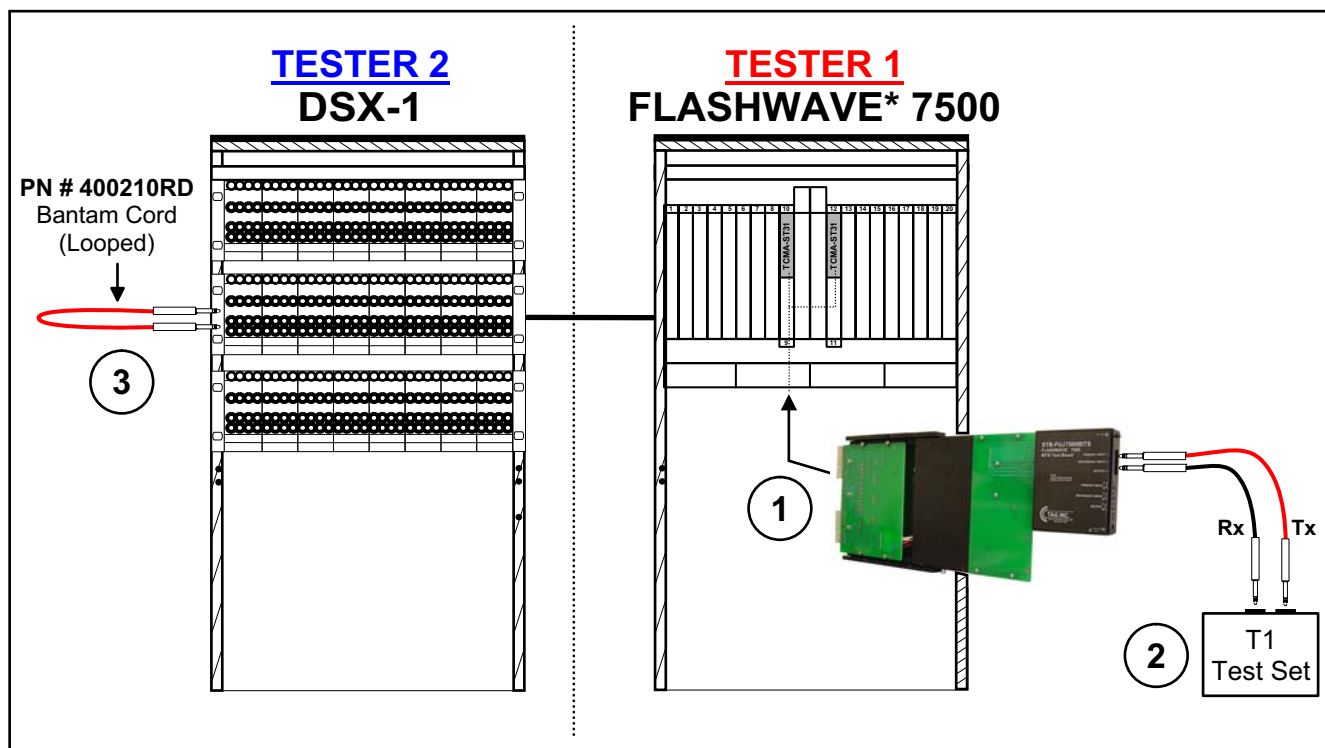
- 1b. Connect test probe (# P-2000) Alligator Clip to Frame Ground.
- 2b. Plug Bantam to Alligator Clips cord (# 400210A) in DSX-1 jack to begin testing.
- 3b. Touch Probe end to "ground the corresponding wiring assignments." The LED on the probe will illuminate to indicate a connection to the STB-FUJ7500BITS Card.

**LED (Primary Input Tip) = DSX-1 (Primary Input Tip)**



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### **Primary and Secondary BITS Test Acceptance Procedure:**

1. Select BITS card slot TCMA-ST31 to begin testing.  
**(CAUTION: Do not force. Verify proper alignment before inserting.)**
2. Using a T1 Test Set, connect bantam cords (# 400210RD)-Transmit (Tx) and (# 400210BK)- Receive (Rx) to the appropriate jacks from the STB-FUJ7500BITS to a T1 test set to perform acceptance testing.
3. Establish communication with Tester 2 at the DSX-1. Determine which circuits you will be testing. Tester 2 will loop the test signal back to Tester 1 with a looped bantam cord (# 400210RD).
4. Tester 1 will verify that the signal transmitted is acceptable for qualification purposes.