

Fujitsu FLASHWAVE® 4100ES DS3 Test Board TB-F4100ESDS3-8 Practice

CONTENTS	
1. GENERAL	1
2. APPLICATION	1
3. PROCEDURE	1
4. PHYSICAL DESCRIPTION/ WIRING	2
5. STEP BY STEP TEST PROCEDURE	3



APPLICATION

The TB-F4100ESDS3-8 provides a quick method to transmit an acceptable test signal from the FLASHWAVE* 4100ES DS3 card slots to the DSX-3. The wiring label indicates Transmit (Tx) and Receive (Rx) 440A jacks for the 8 DS3 circuits. The TB-F4100ESDS3-8 is used on non-powered systems.

Note: The TAG TB-F4100ESDS3-8 is a temporary Test Board. Do not leave it unattended while plugged into a card slot.



GENERAL

This practice describes Telecom Assistance Group's PN # TB-F4100ESDS3-8 Test Board. The TB-F4100ESDS3-8 is used when installing the Fujitsu FLASHWAVE* 4100ES system.

The TB-F4100ESDS3-8 is a temporary test card that plugs into the 4100ES shelf. The Test Board is used to verify wiring and perform DS3 signal acceptance to the DSX-3/4. Acceptance testing determines whether or not the card slots are properly connected through the interconnect wiring and able to Transmit and Receive a good DS3 signal.

See Page 2 for Physical Description/Wiring.



PROCEDURE

The test acceptance procedure typically requires two (2) people for ease of testing. Tester 1 will be located at the Fujitsu FLASHWAVE* 4100ES with the TAG TB-F4100ESDS3-8, and Tester 2 will be at the DSX-3. Tester 2 will then loop the signal back to the TB-F4100ESDS3-8 and determine if the signal is acceptable for system turnup.

See Page 3 for Step-by-Step Test Procedure.



Fujitsu FLASHWAVE® 4100ES DS3 Test Board **TB-F4100ESDS3-8 Practice**

Physical Description / Wiring



Fujitsu FLASHWAVE* 4100ES Shelf

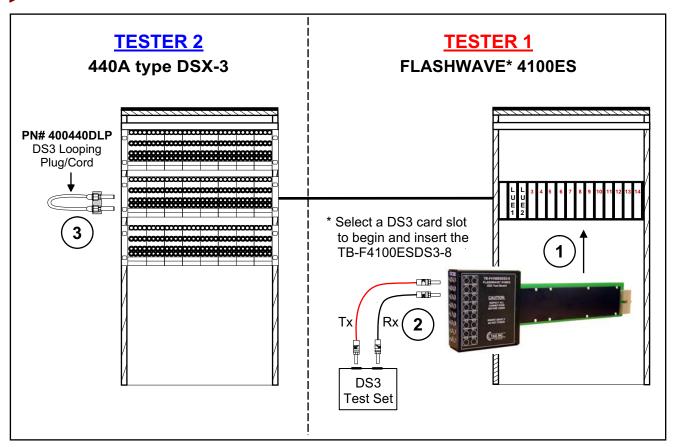


The (TB-F4100ESDS3-8) FLASHWAVE* 4100ES DS3 Test Board will plug into slots configured for DS3



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Step by Step Procedure



Test Acceptance Procedure:

- Select a DS3 card slot to begin testing.
 (CAUTION: Do not force. Verify proper alignment before inserting.)
- 2. Using a DS3 Test Set, connect 440A DS3 cords (PN # 400440ARD) -Transmit (Tx) and (PN # 400440ABK) -Receive (Rx) to the appropriate jacks from the TB-F4100ESDS3-8 to the DS3 test set to perform an acceptance test.
- Establish communication with Tester 2 at the DSX-3. Determine which circuits you
 will be testing. Tester 2 will loop the test signal back to Tester 1 with a DS3 looping
 plug/cord (PN # 400440DLP).
- 4. Tester 1 will verify that the signal transmitted is acceptable for qualification purposes.